

095758-10004

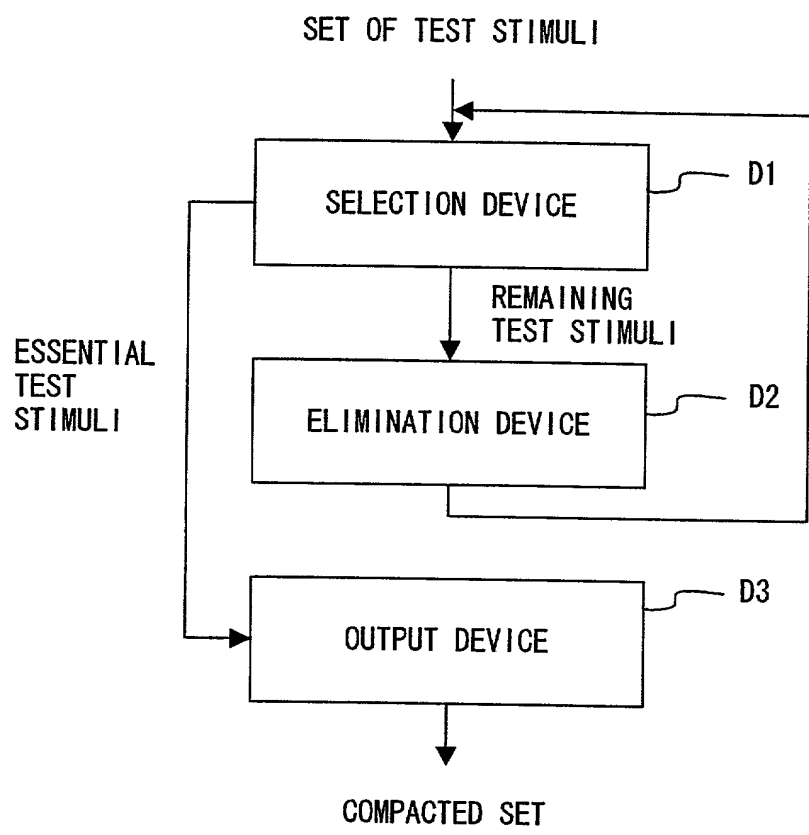


FIG. 1

Test Vector \ Fault	f_1	f_2	f_3	f_4	f_5	f_6	f_7	f_8	f_9	f_{10}	f_{11}	f_{12}
t_1	×	×		×			×					
t_2	×		×	×				×				
t_3		×			×		×			×		
t_4			×			×						
t_5					×				×			
t_6				×			×	×			×	
t_7						×	×					×

F I G. 2

Test Vector \ Fault	f_1	f_3
t_1	X	
t_2	X	X
t_4		X

F I G. 3

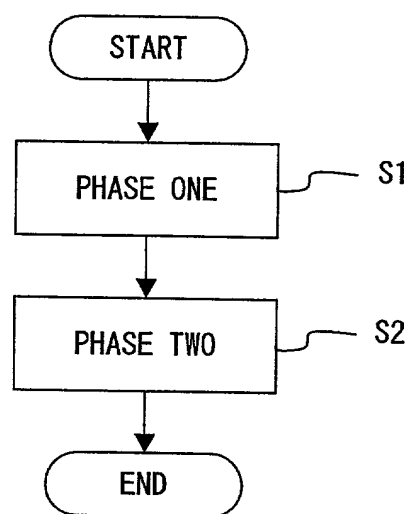


FIG. 4

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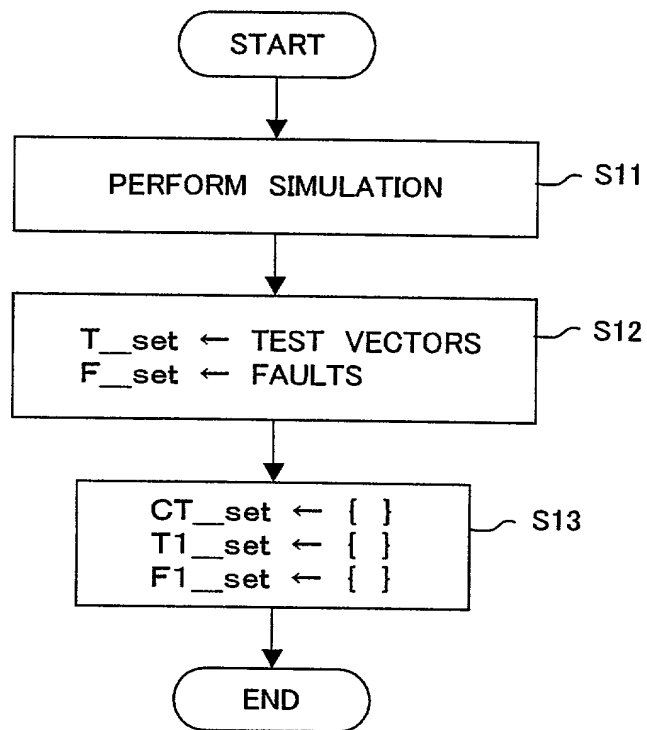


FIG. 5

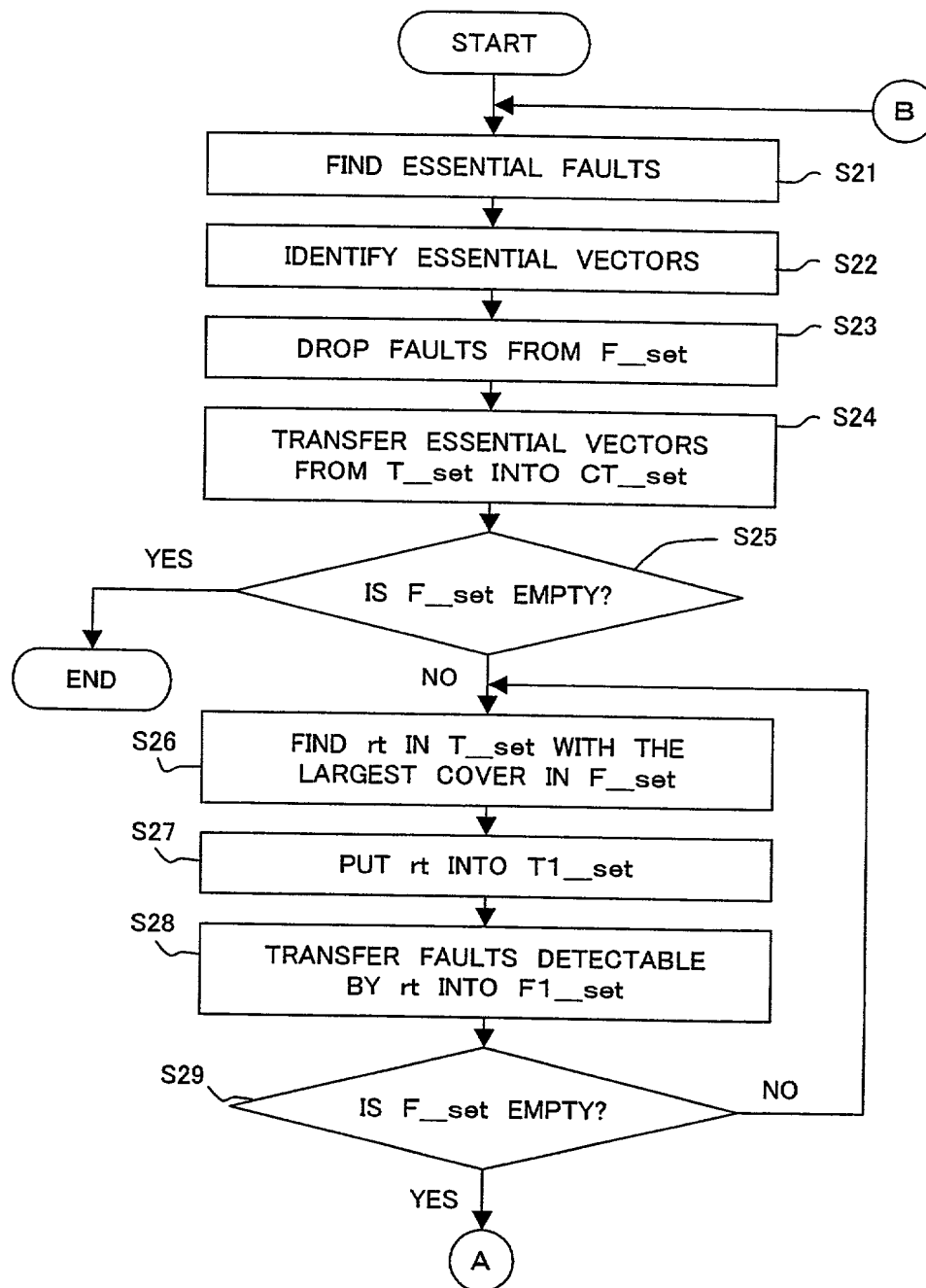


FIG. 6

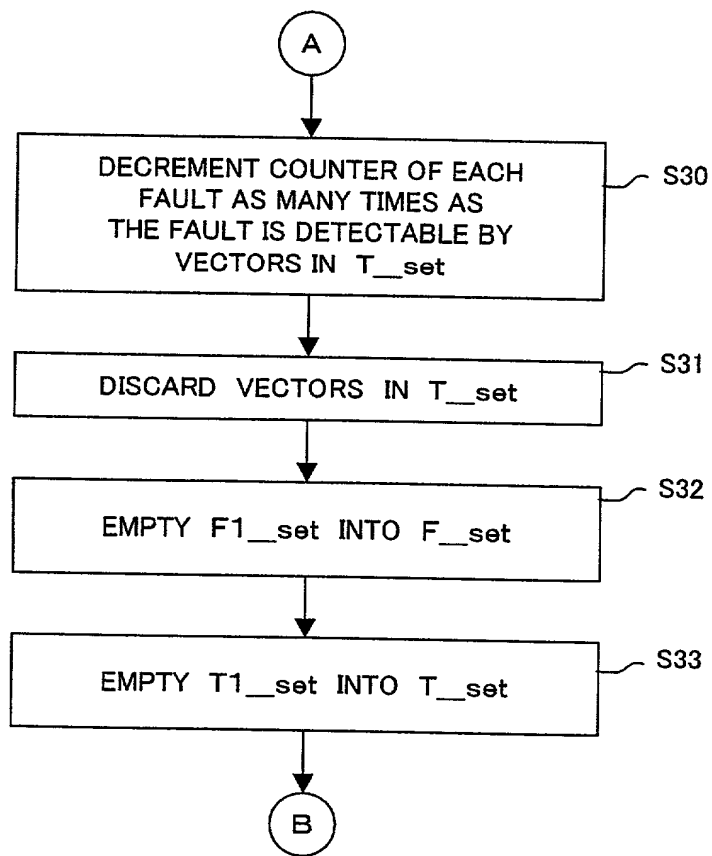
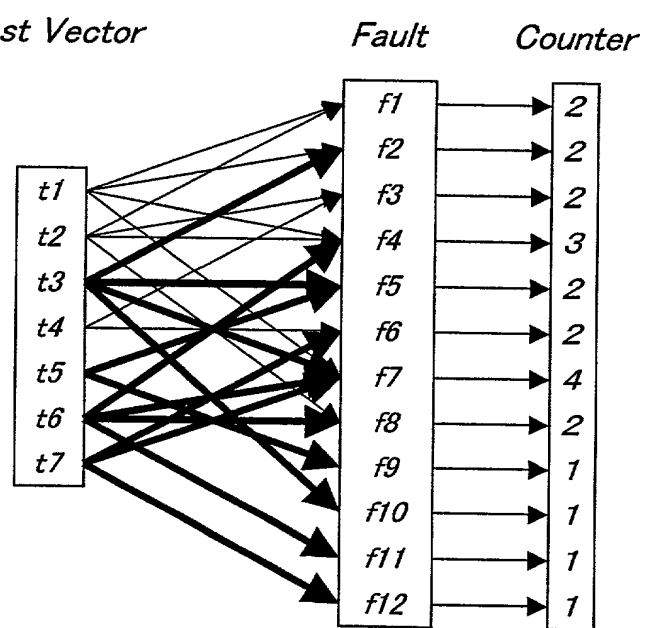
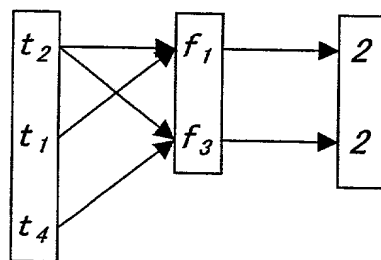


FIG. 7



F I G. 8



F I G. 9

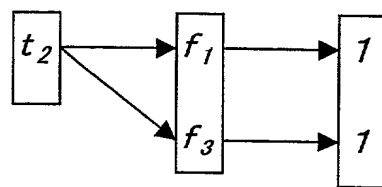


FIG. 10

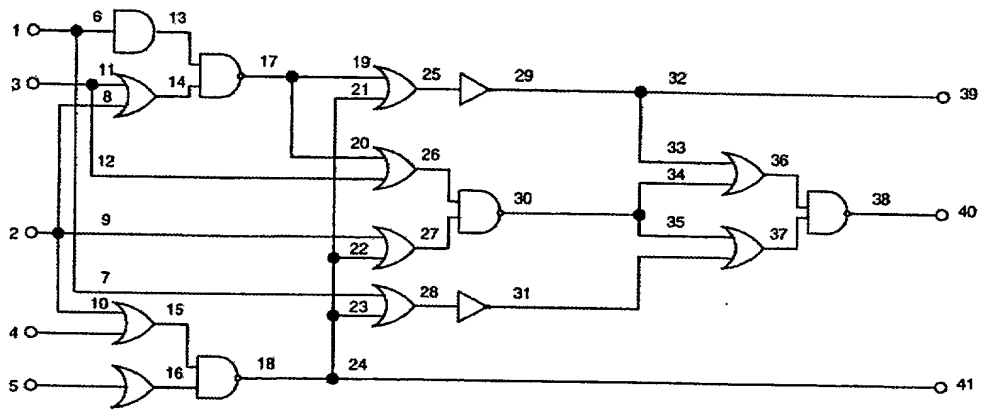


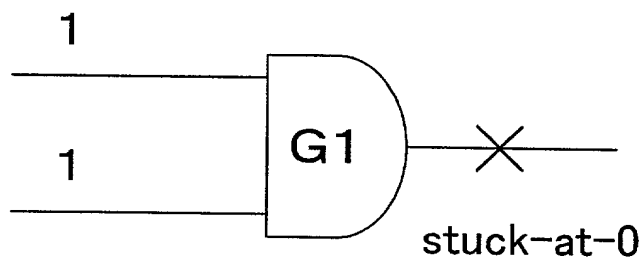
FIG. 11

Test Vector	Pattern
t_1	0 1 2 2 1
t_2	1 1 2 2 1
t_3	2 0 2 0 1
t_4	2 1 2 0 1
t_5	1 0 0 1 1
t_6	1 0 1 1 1
t_7	2 0 2 1 1
t_8	2 1 2 2 0
t_9	2 1 2 2 1
t_{10}	1 0 0 1 0
t_{11}	1 1 0 1 1
t_{12}	0 0 1 1 1
t_{13}	0 1 1 1 1
t_{14}	1 1 0 2 1
t_{15}	1 1 1 2 1
t_{16}	0 1 0 2 0

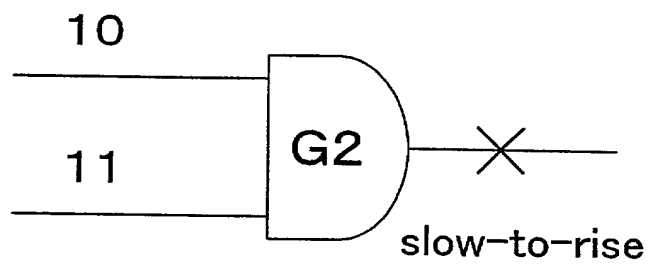
FIG. 12

Fault	Counter	Test Vector	#Fault
<i>1/1</i>	<i>3</i>	<i>t₁</i>	<i>6</i>
<i>5/1</i>	<i>3</i>	<i>t₂</i>	<i>0</i>
<i>6/1</i>	<i>3</i>	<i>t₇</i>	<i>0</i>
<i>8/0</i>	<i>2</i>	<i>t₈</i>	<i>2</i>
<i>12/1</i>	<i>2</i>	<i>t₉</i>	<i>0</i>
<i>13/1</i>	<i>3</i>	<i>t₁₀</i>	<i>2</i>
<i>16/1</i>	<i>3</i>	<i>t₁₁</i>	<i>4</i>
<i>20/1</i>	<i>2</i>	<i>t₁₂</i>	<i>3</i>
<i>26/1</i>	<i>2</i>	<i>t₁₃</i>	<i>6</i>
<i>33/1</i>	<i>2</i>	<i>t₁₄</i>	<i>4</i>
<i>34/1</i>	<i>2</i>	<i>t₁₆</i>	<i>2</i>
<i>36/1</i>	<i>2</i>		

F I G. 1 3



F I G. 1 4



F I G. 1 5

FIG. 16

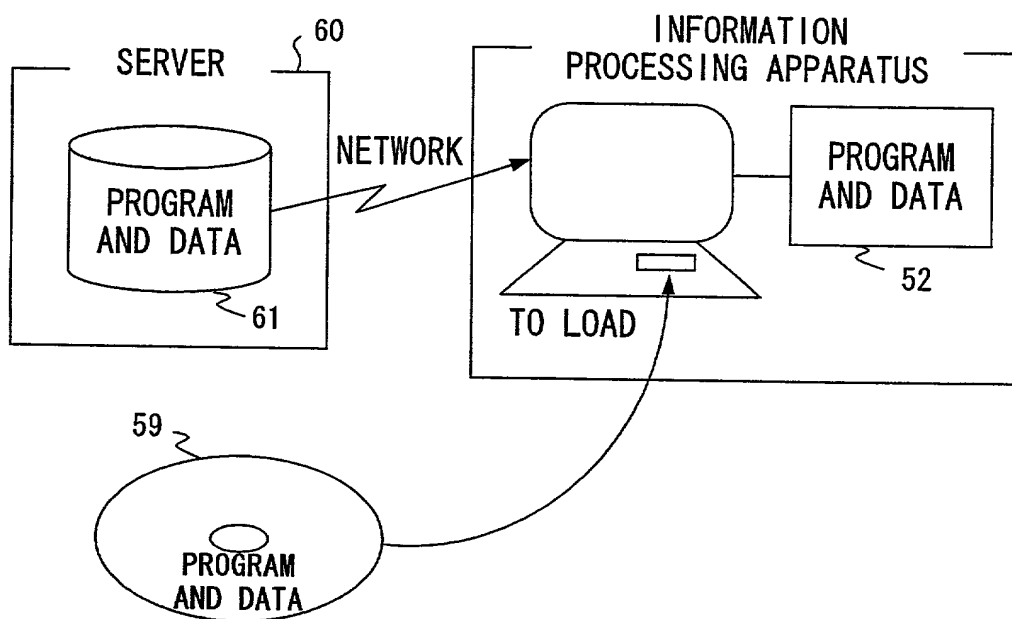


FIG. 17